Electronic Filing System (EFS) Data Electronic Patent Application Submission USPTO Use Only

EFS ID:

16067

Application ID:

10064253

Title of Invention:

METHOD OF SAVING/RESTORING

PROCESSOR STATE AFTER

ENTERING/EXITING DEBUG MODE

First Named Inventor:

Mike Ryken

Domestic/Foreign Application:

Domestic Application

Filing Date:

null

Effective Receipt Date:

2002-06-26

Submission Type:

Utility Patent Filing

Filing Type:

new-utility

Confirmation Number:

0

Attorney Docket Number:

8781-US-PA

cn=Belinda Lee, ou=Registered Attorneys, ou=Patent and

Digital Certificate Holder:

Trademark Office, ou=Department of Commerce, o=U.S.

Government, c=US

Certificate Message Digest:

MJNca9Y90RoPi7fLAVB9iQ==

Total Fees Authorized:

\$452.0

Payment Category:

CC - Credit Card

Credit Card Number:

**********3109

Expiration Date:

04302003

Card Holder Name:

LEE, HUAI-LU

RAM User ID:

EFSPROD

RAM Accounting Date:

2002-06-26

RAM Sequence Number:

475145

RAM Payment Status:

RAM success

Postal Code:

99999

APP ID=10064253

TRANSMITTAL FORM



Electronic Version 1.0.3

Stylesheet Version: 1.0

Attorney Docket

8781-US-

Number:

PA

Submission Type: Utility Patent

Filing

METHOD OF SAVING/RESTORING PROCESSOR STATE AFTER ENTERING/EXITING DEBUG MODE

First Named Inventor: Mike Ryken

SUBMITTED BY

Name:

Dr. Belinda Lee

Registration Number:

46,863

Electronic Signature Mark: /Belinda

Lee/

Date Signed: 20020626

I certify that the use of this system is for OFFICIAL correspondence between patent applicants or their representatives and the USPTO. Fraudulent or other use besides the filing of official correspondence by authorized parties is strictly prohibited, and subject to a fine and/or imprisonment under applicable law.

I, the undersigned, certify that I have viewed a display of document(s) being electronically submitted to the United States Patent and Trademark Office, using either the USPTO provided style sheet or software, and that this is the document(s) I intend for initiation or further prosecution of a patent application noted in the submission. This document(s) will become part of the official electronic record at the USPTO.

Attached Files:

declaration

8781 declaration 2.tif

declaration

8781 declaration 1.tif

specification patent-assignments fee-transmittal

bibd-transmittal

Attached Image File(s): 8781 declaration 2.tif 8781 declaration 1.tif

8781 usf.xml

8781usasgn.xml

8781 usfee.xml

8781 usapds.xml

Comments:

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patents issued thereon.

4/25/02 Date

Sole or First Joint Inventor: Mike Ryken

Citizenship: U.S.A.

Residence and Post Office Address: c/o 2880 Zanker Rd., Suite 104, San Jose, CA 95134, U.S.A

COMBINED DECLARATION AND POWER OF ATTORNEY

As the below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name and that I believe I am an original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

METHOD OF SAVING/RESTORING PROCESSOR STATE AFTER ENTERING/EXITING DEBUG MODE

the:	the specification of which							
X	is attached hereto.							
_	was filed on							
	as Application Serial No and was amended on							
fore any appl Prio	I hereby state that I have diffication, including the claim I acknowledge the duty to dication in accordance with Toleration in accordance with Toleration for patent of application for patentication on which priority is claim Foreign Application(s): NO	ns, as amended by a disclose information itle 37, Code of Federity benefits under or inventor's certificant or inventor's certification of inventor's certification of inventor's certification.	ny amendment reformation which is material which is material was also with the state of the stat	erred to above. to the patentability of this 1.56(a). States Code, § 119 of any I have also identified below ing date before that of the				
	reby appoint the following a sact all business in the Pater							
	Belinda Lee Jiawei Huang Charles C.H. Wu	(Reg. No. 46,863) (Reg. No. 43,330) (Reg. No. 39,081)	Marrina Mei	(Reg. No. 44,935)				
SEND CORRESPONDENCE TO:		DIRECT TELEPHONE CALLS TO: (Name and Telephone Number) Belinda Lee						
JIANQ CHYUN Intellectual Property Office 7F1, No. 100, Roosevelt Rd., Sec. 2, Taipei 100, Taiwan, R.O.C.								
7	TEL: 886-2-2369 2800							
F	FAX: 886-2-2369 7233							

FEE TRANSMITTAL

Electronic Version 1.1.0 Stylesheet Version: 1.0

Patent fees are subject to annual revisions on or about October 1st of each year.

Small Entity

Small Business Concern

TOTAL FEES AUTHORIZED: \$ 452

BANK (CREDIT) CARD INFORMATION:

Credit Card Number:

3109

Expiration Date:

20030430

Authorized Name:

LEE, HUAI-LU

Billing Address:

99999

BASIC FILING FEE

Fee Description	Fee Code	Fee Paid		
Utility Filing Fee	201	\$ 370		

Subtotal For Basic Filing Fee: \$ 370

EXTRA CLAIM FEES

	Fee Code	Fee	Extra Claims	Fee Paid
Total Claims: 11	203	\$ 9	0	\$ O
Independent Claims: 4	202	\$ 42	1	\$ 42

Subtotal For Extra Claims Fees: \$ 42

ADDITIONAL FEES

Fee Description	Number	Quantity	Fee Code	Amount	Fee Paid
Recording Each Patent Assignment Per Property Fee	00000000	1	581	\$ 40	\$ 40

Subtotal For Additional Fees: \$ 40